AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended): A transmitter system, comprising:

a spreading code generator that produces a spreading code;

a spreader that combines the spreading code with an input signal to provide a spread input signal;

~~a signal converter that converts the frequency converted spread input signal from a first domain to a second domain to provide a converted spread input signal;~~

a mixer for frequency converting the spread input signal to provide an upconverted spread input signal;

a signal converter that converts the upconverted spread input signal from a first domain to a second domain to provide a converted spread input signal;

a despreader that despreads the upconverted spread input signal to provide <u>a despread</u> ~~the input~~ signal ~~in the second domain~~; and

an antenna that transmits the despread ~~input~~ signal.

2-4. (Cancelled).

5. (Currently Amended)   The <u>transmitter</u> system of claim 1, further comprising a feedback loop coupling the despreader to the spreader for time aligning the despreading with the spreading.

6. (Currently Amended)   The <u>transmitter</u> system of claim 1, wherein the first domain is one of a digital domain and an analog domain and the second domain is the other of the digital domain and the analog domain.

7. (Cancelled)

8. (Currently Amended)   The <u>transmitter</u> system of claim 1, wherein the signal converter is one of a delta-sigma analog-to-digital converter (ADC) and a delta-sigma digital-to-analog converter (DAC).

9. (Currently Amended)   The <u>transmitter</u> system of claim 1, further comprising a clipping component that reduces peaks associated with the spread input signal, the despreader mitigates degradation and out-of-band (OOB) emissions associated with the peak reduction.

10. (Currently Amended)   The <u>transmitter</u> system of claim 1, wherein at least one of the spreader and the despreader circuit comprises a mixer.

11. (Cancelled).

12. (Cancelled).

13. (Previously Presented)   A signal conversion system comprising:
        a spreading code generator that produces a direct sequence spread spectrum (DS-SS) spreading code;
        a spreading circuit that receives an input signal and combines the input signal with the DS-SS spreading code to provide a spread input signal;
        a clipping component that reduces peaks associated with the spread input signal; and
        a despreading circuit that despreads the peak reduced spread input signal.

14. (Original) The system of claim 13, wherein at least one of the spreading circuit and despreading circuit comprises a mixer.

15. (Original) The system of claim 13, further comprising a signal converter that converts the spread input signal from a first domain to second domain, the signal converter being one of a digital-to-analog converter (DAC) and an analog-to-digital converter (ADC).

16. (Original) The system of claim 15, the signal converter being one of a delta-sigma DAC and a delta-sigma ADC.

17. (Previously Presented)   The system of claim 15, further comprising a second signal converter for converting the spread input signal from the second domain to the first domain.

18. (Original) The system of claim 15, further comprising a mixer for frequency converting the spread input signal one of before signal conversion and after signal conversion.

19. (Previously Presented): A method for transmitting a signal, comprising:
     spreading a digital signal with a spreading signal code;
     converting the digital spread signal to an analog signal;
     modulating the analog signal to produce an upconverted analog signal;
     despreading the upconverted analog signal to provide a despread signal; and
     transmitting the despread signal.

20-24. (Cancelled).

25. (Original) The method of claim 19, further comprising clipping the signal to reduce peaks associated with the signal.

26. (Previously Presented): A communication device comprising:

    means for generating a direct sequence spread spectrum (DS-SS) spreading code;

    means for combining the DS-SS spreading code with an input signal to produce a spread input signal;

    means for clipping the spread input signal to remove peaks;

    means for converting the spread input signal from a first domain to a second domain; and

    means for dispreading the spread input signal in the second domain.

27. (Cancelled).